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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Taiyuu Miyamoto

Application No.: 09/998,830

Filed: December 3, 2001

For: MICROCOMPUTER

) **MAIL STOP AMENDMENT**
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) Group Art Unit: 2183
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) Examiner: KEVIN P. RIZZUTO
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) Confirmation No.: 7164
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RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: May 16, 2005

Sir:

This election is filed in response to the Office Action dated February 17, 2005 (Paper No. 20050214). In the Office Action, the Examiner identified three distinct inventions. The Examiner required the Applicant, under 35 U.S.C. §121, to elect a single invention for prosecution. The three inventions identified by the Examiner are:

- I) Claims 1, 2, 5, 6, and 8 are drawn to hardware to prevent external access or commands to change modes or data of a processor via an externally input data;
- II) Claims 3 and 4 are drawn to memory map selection hardware;
- III) Claim 7 is drawn to a voltage regulating system to prevent spurious signals.

Pursuant to the Examiner's requirement, the Applicant elects Group I, claims 1, 2, 5, 6 and 8. It is understood that claims 3, 4 and 7 will be withdrawn from prosecution. Applicant reserves the right to file divisional applications.

An earlier examination and favorable action on claims 1, 2, 5, 6 and 8 are respectfully requested.

In the event this paper is not considered to be timely filed, Applicant hereby petitions for an appropriate extension of time. The Commissioner is authorized to charge payment for any additional fees which may be required with respect to this paper to our Deposit Account No. 02-4800.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: May 16, 2005

By:


Ellen Marcie Emas
Registration No. 32,131

P.O. Box 1404
Alexandria, Virginia 22313-1404
(703) 836-6620